



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Wang

Appl. No.: 10/668,249

Filed: September 24, 2003

For: **System and Method to Relieve  
ESD Requirements of NMOS  
Transistors**

Confirmation No.: 4291

Art Unit: 2836

Examiner: Danny Nguyen

Atty. Docket: 1875.5090000

**Amendment and Reply Under 37 C.F.R. § 1.111**

***Mail Stop Amendment***

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated **April 21, 2006**, Applicants submit the following Amendment and Remarks.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefore (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.